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ATM traffic generator card. An integrated solution

Hontas, S., Tselikis, G., Tompros, S., Gianniadakis, J., Andritsos, S., Loukatos, D. A.
 Dept. of Electr. & Comput. Eng., Inst. of Commun. & Comput. Syst., Athens, Greece;

This paper appears in: [Computers and Communications, 1998. ISCC '98. Proceedings Symposium on](#)

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Abstract

In this paper the design and the implementation of an ATM traffic generator system is presented. The principles of this document follow the architecture presented in Hontas et al. (1997). It is implemented with a careful allocation of functions between hardware and software, in a way that it can support various traffic profiles (VBR and CBR). The system is composed of a basic traffic generator and ATM interface and four peripheral cards for the support of different physical interfaces. The architecture is designed to be open to any future modifications concerning signalling and functionality. The software of the system is developed under Windows NT.

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ATM burst traffic generator

Chu, P.P.

Dept. of Electr. Eng., Cleveland State Univ., OH , USA;

This paper appears in: [VLSI, 1995. Proceedings., Fifth Great Lakes Symposium on](#)
Publication Date: 16-18 March 1995

On page(s): 262 - 265

Meeting Date: 03/16/1995 - 03/18/1995

Location: Buffalo, NY

INSPEC Accession Number:5143931

Digital Object Identifier: 10.1109/GLSV.1995.516064

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Abstract

The Markov modulated process is widely used to model ATM (Asynchronous Transfer Mode) sources because it can emulate the bursty and correlated traffic arrival. We propose a hardware generator emulating the bursty behavior of the Markov process and develop circuits for a modulated process and an N-state homogeneous Markov modulated process. The generator traffic imitating cell arrival of various voice and video sources. Our test circuit can be fitted on a standard sized FPGA chip and can generate up to 20M cell arrivals per second.

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Author Keywords

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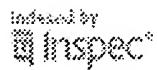
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